Dr. Zhila Amini-Sheshdeh

Assistant Professor (Since 2014)
Engineering Department of Alzahra University

- Address: Room 117, 1th Floor, Department of Engineering, Kharazmi Building, Alzahra University, Tehran, Iran.
- Tel: +9821-85692176
- E-mail: *zh.amini@alzahra.ac.ir*

Education:

- B. Sc.: Electrical Engineering, Sharif University of Technology, 2000-2004.
- M. Sc.: Electrical Engineering, Tarbiat Modarres University, 2004-2006.
- Ph. D. : Electrical Engineering, Tarbiat Modarres University, 2007-2013

Research Interest:

- Design of digital VLSI circuits.
- Reliability in nano scale CMOS.

Journal Papers:

- Zh. Amini-Sheshdeh and A. Nabavi, "New HCI and TDDB Sensors Based on Transition Time Monitoring," International Journal of Science and Technology (Scientia Iranica), Transaction D: Computer Science & Engineering and Electrical Engineering, Vol. 22, No. 6, December 2015.

- Zh. Amini-Sheshdeh and A. Nabavi, "A New On-chip Sensor Design for NBTI Using Slew Rate Monitoring," International Journal of Science and Technology (Scientia Iranica), Transaction D: Computer Science & Engineering and Electrical Engineering, Vol. 20, No. 6, December 2013.

- Zh. Amini-Sheshdeh and A. Nabavi, "Design of Improved Reliability Nano Circuits with Mixed NBTI and HCI Aware Gate Sizing Formulation," Wiley

IEEJ Transactions on Electrical and Electronic Engineering Journal, Vol. 8, No. 6, November 2013.

Conference Papers:

- ژیلا امینی ششده و عبدالرضا نبوی، " سنسورتشخیص عدم تطابق ترانزیستورها بااستفاده از زمان گذر" پانزدهمین همایش دانش آموختگان فناوری نانو، اردیبشهت ۱۳۹۳
- ژیلا امینی ششده و عبدالرضا نبوی، " شناسایی تراشه ها با استفاده از زمان گذر ،" کنفرانس بین المللی یافته های نوین پژوهشی در مهندسی برق و علوم کامپیوتر، شهریور ۱۳۹۴
- ژیلا امینی ششده، "اثرات پیری در آینه های جریان " ، کنفرانس بین المللی مهندسی برق، شهریور ۱۳۹۶

- Zh. Amini-Sheshdeh, A. Nabavi, "An On-Chip NBTI Sensor with Rise Transition Time Monitoring Circuit," in Proc. of IEEE (Iran Section) 7th Iranian Conference on Electrical and Electronics Engineering (ICEEE), August 2015.
- Zh. Amini-Sheshdeh, A. Nabavi, "A Novel Sensor for Prediction of Aging Failure," in Proc. of IEEE 3th International Conference on Computational Intelligence, Modeling and Simulation (CIMSIM), September 2011.
- Zh. Amini-Sheshdeh, A. Nabavi, "A Blind Timing Acquisition Algorithm for DS-UWB systems," in Proc. of 5th International Conference on Electrical and Electronics Engineering (ELECO), December 2007.
- Zh. Amini-Sheshdeh, A. Nabavi, "Implementation of DS-Ultra wideband Timing Acquisition ON FPGA," in Proc. of Electronic Circuits and Systems Conference (ECS), September, 2007.
- B. Eghbalkhah, Zh. Amini- Sheshdeh and Ali Afzali-Kusha "A New Preamble-less Timing Synchronization Method for OFDM Systems under Multi-Path Channels," in Proc. of IEEE, Design & Technology of Integrated Systems (DTIS), September, 2007.
- B. Eghbalkhah, Zh. Amini- Sheshdeh and Mehdi Ehsani-Nick, "High-Speed Parametric FPGA Implementation of FFT/iFFT Blocks for OFDM Transceivers," in Proc. of Iranian Conference on Electrical Engineering (ISCEE), September, 2006.
- Zh. Amini- Sheshdeh, A. Nabavi and M. B. Ghaznavi- Ghoushchi, "Acquisition of Wideband Direct-Sequence Spread Spectrum Signals In System C, " in Proc. of Iranian Conference on Electrical Engineering (ISCEE), September, 2006.

- Zh. Amini- Sheshdeh, S. Choobkar and A. Nabavi, “A Low Noise Amplifier for Ultra-wideband Systems in 0.13μm CMOS Technology,” in Proc. of IEEE, International Conference on Communication, Circuits and Systems (ICCCAS), June, 2006.

Patents:

- ژیلا امینی ششده و عبدالرضا نبوی ، " طراحی سنسوری برای پدیده ناپایداری ناشی از بایاس منفی و دما در ترانزیستور PMOS با استفاده از تغییرات زمان گذر"، ثبت شده در اداره کل مالکیت صنعتی ایران، شماره ثبت ۷۷۰۳۵، زمان ثبت ۱۳۹۱/۰۷/۱۲

Work Experience:

- **Pardis Novel Processing Technology Company:**

✓ Winter 2007, Design and FPGA implementation of ASI to STM1 digital interface and Vice Versa.
✓ Fall 2007, Design and FPGA implementation of G.703 to STM1 digital interface and Vice Versa.
✓ November 2006 – March 2007, R&D Engineer, Digital Design Section of Pardis Novel Processing Technology Company, Tehran, Iran.

- **University of Tehran:**

✓ Fall 2004 – fall 2005, Technical Study, Design and FPGA Implementation of PCI Express Standard. Primary Investigator: Dr. Ali Afzali-Kusha, Funding Agency: High technology Industries Center.

- **Resana-Afzar Sharif Company:**

✓ Summer 2002, Design and hardware test of answering machine for phone line.
✓ Fall 2002, Design and FPGA implementation of a SRAM Controller for Data Logger.
✓ April 2003 – September 2003, R&D Engineer, Digital Design Section of Resana Afzar Sharif Company Ltd., Tehran, Iran.